

JC474 U.S.  
09/682131  
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ISSUE CLASSIFICATION

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**U.S. UTILITY Patent Application**

O.I.P.E.

**PATENT DATE**

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/682131	F	257	724	283815	Chu

## APPlicants

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TIME

## Semiconductor integrated circuit device with EMI prevention structure

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input checked="" type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent No. _____				<b>ISSUE FEE</b>	
	(Assistant Examiner)	(Date)		Amount Due	Date Paid
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<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				<b>ISSUE BATCH NUMBER</b>	
	(Legal Instruments Examiner)	(Date)			

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